

JADAVPUR UNIVERSITY
KOLKATA – 700 032

Ref. No.: REC/N/339/17

Dated : 14/09/2017

C I R C U L A R

A Walk-in-interview will be held in the ETCE Department, 3rd Floor, SMDP-III Lab., Jadavpur University, Kolkata – 700032 on 23rd October 2017, 3 pm onwards, for the recruitment of One Project Associates in the project entitled "Special Manpower Development Programme Chip to System design" under the supervision of Prof. Sayan Chatterjee, Department of Electronics & Telecommunication Engineering Department, Jadavpur University. Detail advertisement is available in University website [www.jaduniv.edu.in].

SNo	Designation	Number of posts	Qualification & Desirable Experience	Consolidated Salary	Schedule of Interview
1.	Project Associate	One	B.E, B.Tech. (ECE) having working experience in VLSI related software. Age limit : Maximum age as on 23/10/2017 , 2017 is 30. Desirable: 1. Design knowledge in RF VLSI, Physical VLSI design 2. Design experience in MOS related circuit 3. Knowledge of Cadence/ Synopsys	Rs. 20,000/- per month	Walk- in Interview will be held at SMDP laboratory Interview Date : 23 rd October, 2017 Reporting time 3-4pm at IC center, Dept. of ETCE, Jadavpur University Interview time from 4pm onwards

Interested candidates may appear in person directly in the interview with original as well as attested copies of Mark sheets, Certificates and Testimonials which are to be produced at the time of interview. No TADA is admissible.

Sayan Chatterjee
14/9
Registrar(Act.)