

**JADAVPUR UNIVERSITY**  
**KOLKATA – 700032**

**Admission Notice FET/2/15 dt. 26.05.2015**

**INFORMATION BROCHURE FOR 3-YEAR B.TECH. COURSE IN  
INSTRUMENTATION & ELECTRONICS ENGINEERING FOR THE SESSION 2015-16**

**Eligibility:**

**3-year B.Tech. in, Instrumentation & Electronics Engineering:-**  
Candidate must have passed the B.Sc. Examination (3-year course) of a University with Honours as per following criteria:

**I) Honours and subsidiary Subjects:**

<b>Serial No.</b>	<b>Honours subject (3-years B.Sc. course)</b>	<b>Subsidiary Subjects</b>
1.	Physics	Mathematics
2.	Instrumentation Science	Mathematics
3.	Electronics Science	Mathematics
4.	Computer Science	Physics
5.	Mathematics	Physics
6.	Statistics	Physics

**II) Minimum marks for eligibility:**

60% marks (50% for SC/ST students) in the Honours subject in the 3-years B.Sc. course.

**III) Admission Test:**

The paper will consist of two groups as detailed below:

Group A: Physics (100 marks)

Group B: Mathematics (50 marks)

**IV) Syllabi for the admission test:**

The Syllabi of the above subjects should be

**Group A:** Physics as per Physics (Subsidiary) syllabus of B.Sc. (3-Years course) level.

**Group B:** Mathematics as per Mathematics (Subsidiary) syllabus of B.Sc. (3-years course) level.

Candidates who have appeared / are due to appear at the qualifying examination may also apply and such candidates, if selected, will be admitted only if they can submit valid proof of their passing the qualifying examination with required percentages of marks at the time of interview. However, if the results of the qualifying examination as stated above is not published at the time of interview, the candidate shall have to produce a certificate from the Principal of the College last attended stating that he/she (candidate) has appeared in the B.Sc. final examination with the honours and subsidiary subjects mentioned in it. In that case if, at the interview, the candidate is selected for admission, he/she will be given provisional admission and he/she shall have to submit the original and attested copy of B.Sc. (Hons.) Final mark-sheet within 3 months from the date of commencement of 1<sup>st</sup> year B.Tech classes, failing which or if the

candidate is found to be not eligible as per the mark-sheet, his/her provisional admission will be cancelled.

1. Downloaded Application Form, Information Brochure and Pay-in-Slip for 3-year B.Tech. course in Instrumentation & Electronics Engg. (IEE) for the session 2015-16 will be available from the University website [www.jaduniv.edu.in](http://www.jaduniv.edu.in) from 28.05.2015 to 15.06.2015. Last Date of receiving of Application Form is 16.06.2015.

2. Intake of the Course: General – 14, SC – 4, ST – 1, PD – 1.

Candidates are advised to download the Information Brochure and Pay-in-Slip of State Bank of India for deposit of Application Fee. The amount of application fee of Rs.300/- for the course of study is to be deposited through any branch of State Bank of India by filling up the Pay-in-slip. The Journal Number must be legible in the pay-in-slip, which is needed to be used in Application Form & for other references. The applicants are requested to keep the counterfoil of the deposit slip for future reference. Candidates should carefully read the instructions given in the Information Brochure before filling up the Application Form.

3. Properly filled in Downloaded Application Form, Application part of Pay-in-Slip and self attested Xerox copy of all related documents (mentioned in the Application Form) is to be sent by Post TO THE PRINCIPAL SECRETARY, FACULTY COUNCIL OF ENGG. & TECH., JADAVPUR UNIVERSITY, KOLKATA – 700032, within the last date of receiving of application form (i.e. 16.06.2015).
4. List of eligible candidates for Admission Test available in the University website: 18.06.2015.
5. Eligible candidates for Admission Test are requested to bring Admit Card for the Admission Test downloaded from the University Web site and properly filled up by the candidate and all original documents along with one set of self attested Xerox copy during the Admission Test.
6. Date & Time of Admission Test: 23.06.2015 (Tuesday) at 12-00 noon.

7. After verification of Original Documents, Results of the Admission Test (1<sup>st</sup> Selection List along with Waiting List) will be published on: 24.06.2015 in the University website and Departmental Notice Board.
8. Venue of Admission Test: Instrumentation & Electronics Engineering Department, Jadavpur University Second Campus, PL-8, BL-LB, Sec-III, Salt Lake City, Kolkata – 700 098. (Near Chinrigata Bus stop).
9. Date of Admission: From the 1<sup>st</sup> Selection List: 02.07.2015 in the F.E.T. Office, J.U. Main Campus, from 11 a.m. to 1-30 p.m. with all original and attested Xerox copies and 5 copies passport size photo.
10. Admission Fee will be required at the time of admission: Rs.2660/- in cash.
11. Date of Publication of Waiting List on 08.07.2015, if vacancy exists.
12. Date of Admission from Waiting List (if any) on: 10.07.2015.
13. Commencement of Class : 13.07.2015
14. Application Form & Pay-in-Slip are available in our website.

SC/ST reservation is as per rules. It is to be noted that Ragging is prohibited as per decision of the Supreme Court of India in Writ Petition No. (C) 656/1998 – Principal Secretary, F.E.T.